

What is claimed is:

1. An imaging device which generates pixel signals for pixels which form an image and supplies said pixel signals to a plurality of correction means for correcting said pixel signals, the imaging device comprising:

a plurality of photoelectric conversion means which are arranged vertically and horizontally and generate pixel signals by photoelectric conversion;

supply means which supplies pixel signals generated by said photoelectric conversion means to correction means different from the correction means to which pixel signals generated by adjacent photoelectric conversion means are supplied.

2. An imaging device according to claim 1, wherein said imaging device is a CMOS (Complementary Meta-Oxide Semiconductor).

3. An imaging device according to claim 1, wherein said correction means is a CDS (Correlated Double Sampling) processing circuit, and

said supply means supplies pixel signals generated by said photoelectric conversion means to a CDS processing circuit different from the CDS processing circuit to which pixel signals generated by the adjacent photoelectric conversion means are supplied.

4. An imaging device comprising:

a plurality of pixels which are arranged in an imaging area and generate a signal of a level corresponding to the amount of received light; and

a plurality of signal lines each of which is arranged for each column of said plurality of pixels,

wherein one pixel column of said plurality of pixels includes at least a first pixel and a second pixel,

a signal from said first pixel is read out to a signal processing circuit which is provided at an end of a first signal line included in said plurality of signal lines, through said first signal line, and

a signal from said second pixel is read out to a signal processing circuit which is provided at an end of a second signal line included in said plurality of signal lines, through said second signal line different from said first signal line.

5. An imaging device according to claim 4,  
wherein said signal processing circuit includes an AD conversion circuit.